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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/674,016	09/30/2003	Satoru Miyagi	100021-00133	2380		
4372 75	90 09/07/2005		EXAMINER			
ARENT FOX PLLC			WELLS, KENNETH B			
1050 CONNEC	TICUT AVENUE, N.W.					
SUITE 400			ART UNIT	PAPER NUMBER		
WASHINGTON, DC 20036			2816			
			DATE MAILED, 00/07/000	DATE MAILED: 00/07/0005		

DATE MAILED: 09/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	•	Applicat	on No.	Applicant(s)			
Office Action Summary		10/674,0	16	MIYAGI, SATORU			
		Examine	r	Art Unit			
		Kenneth	B. Wells	2816			
Period fo	The MAILING DATE of this communi or Reply	cation appears on th	e cover sheet with the	correspondence addr	ess		
A SH WHIC - Exter after - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR CHEVER IS LONGER, FROM THE MAN INSIGNS of time may be available under the provisions of SIX (6) MONTHS from the mailing date of this communication period for reply is specified above, the maximum state to reply within the set or extended period for reply very received by the Office later than three months and patent term adjustment. See 37 CFR 1.704(b).	AILING DATE OF T of 37 CFR 1.136(a). In no er unication. tutory period will apply and v will, by statute, cause the ap	HIS COMMUNICATIC vent, however, may a reply be t vill expire SIX (6) MONTHS fror plication to become ABANDON	ON. Itimely filed In the mailing date of this communities IED (35 U.S.C. § 133).			
Status		•					
2a)⊠	Responsive to communication(s) filed This action is FINAL . 2 Since this application is in condition to closed in accordance with the practice.	tb) This action is it for allowance excep	t for formal matters, p	•	nerits is		
Dispositi	on of Claims			•	,		
5)□ 6)⊠ 7)□	Claim(s) <u>1,4,6-13,15-17,19,20 and 2.</u> 4a) Of the above claim(s) is/ar Claim(s) is/are allowed. Claim(s) <u>1,4,6-13,15-17,19,20 and 2.</u> Claim(s) is/are objected to. Claim(s) are subject to restrict	e withdrawn from co 3-28 is/are rejected.	onsideration.				
Applicati	on Papers						
10)[The specification is objected to by the The drawing(s) filed on is/are: Applicant may not request that any object Replacement drawing sheet(s) including The oath or declaration is objected to	a) accepted or betion to the drawing(s) the correction is requi	be held in abeyance. So	ee 37 CFR 1.85(a). bjected to. See 37 CFR			
Priority u	ınder 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
2) Notic 3) Inform	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (P nation Disclosure Statement(s) (PTO-1449 or I r No(s)/Mail Date		4) Interview Summar Paper No(s)/Mail [5) Notice of Informal 6) Other:		52)		

Application/Control Number: 10/674,016

Art Unit: 2816

1. The amendment filed on 7/19/05 has been received and entered in the case.

- 2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 3. Claim 8 is objected to because of the following informalities: in claim 8, line 20, "a" should be changed to --the-- for proper antecedent basis. Appropriate correction is required.
- 4. Claims 1, 4, 6-13, 15-17, 19, 20 and 23-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Mashiko in view of Mizuno et al.

See paragraph four of the final rejection mailed on 12/1/04 for the details of Mashiko.

As to the new limitation added to the claims concerning the wave shaping circuit, this does not distinguish over Mashiko because it is well-known in the art to control a cut-off FET using a waveshaping circuit, which has an output rising slower than its input. See Figs. 24 and 25 of Mizuno et al for such a teaching. In

Fig. 24, the output of circuit PSC is shown rising more slowly than the signal rise of the input signal REQ. The motivation for using the teachings of Mizuno et al to control the cut-off FET of Mashiko in the manner now recited in the claims is to simply obtain the benefits/advantages described in Mizuno et al.

As to the new limitation that the waveshaping circuit includes a high threshold final stage MIS field effect transistor, this also does not distinguish over the combination of references because one skilled in the art will easily recognize that the low threshold transistors in Mashiko are only those inside the logic circuit 11 (see Fig. 1 of Mashiko). All other transistors in the Mashiko circuit will be formed so as to have high threshold values (because they need to have low leakage currents), as will be recognized by the person skilled in the art.

The further limitations of the claims, e.g., forming the high threshold FET and the level conversion circuit as a single module, are all deemed to be obvious variations of the Mashiko/Mizuno combination. Therefore, these further claim limitations do not distinguish patentably over the above-noted combination of these two references.

5. Applicant's arguments filed on 7/19/05 have been fully considered but they are not persuasive.

The argument that in Mashiko the transistors of circuits 13 and 15 are not high threshold transistors is not persuasive because it is incorrect, i.e., the 0.2V, 0.3V threshold transistors described by Mashiko are not the high threshold transistors, but rather the low threshold transistors inside circuit 11.

The further argument on lines 10-19 of page 14 of applicant's response is also not persuasive (because the combination of circuits of 13 and 15 in Mashiko can be interpreted as the claimed level conversion circuit).

6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, THIS ACTION IS MADE FINAL. See MPEP § 706.07(a).

Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the

THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kenneth B. Wells whose telephone number is (571)272-1757. The examiner can normally be reached on Monday through Friday from 8:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P.

Callahan, can be reached at (571)272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information

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applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kenneth B. Wells Primary Examiner Art Unit 2816

September 2, 2005